

RD GUI/Infra T2+

Job Description Summary

This position is a software programmer who will be responsible for Conformal internal infrastructure programming including regression system, web/GUI programming, code review (Review Board) system, continuous integration and others. The candidate must be a strong Python programmer and C/C++ programming.

Job Description:

The role will be responsible for Conformal internal infrastructure programming. The work includes self-development system like regression, web-based utilities, Conformal tool GUI (Tcl/Tk, Qt and Web based) programs. The task may also include open source adoption and deployment inside Conformal team like code review system (Review board, <https://www.reviewboard.org/>), Jenkins (<https://www.jenkins.io/>), and several others.

If you are CS background with good programming skills and want to be part of semiconductor industry, this is your great opportunity to join us to move the industry forward together. You will be working with top customers who create best smartphones, best autonomous cars, best foundry service and best cutting edge products in the world.

Requirements:

- Fast learning and passion in problem solving,
- good at C/C++ or Python programming

PV Python programmer, T2+

Job Description Summary

This position is a product validation engineer who will be a Python programmer for Conformal programmatic testing. No EDA or IC design experience is needed although they can be helpful in taking this challenge.

Job Description:

The role will be responsible for product validation with automation. The role will be writing Python programs to automate the test with best coverage. The program will include testcase generation and executing the tests by running the Conformal and Cadence tools then generate reports by analyzing the result.

If you are CS background with good programming skills and want to be part of semiconductor industry, this is your great opportunity to join us and learn how IC design works and elevate yourself by your daily work. You will be working with top customers who create best smartphones, best autonomous cars, best foundry service and best cutting edge products in the world.

Requirements:

- Fast learning and passion in problem solving,
- good at Python programming

- No EDA/EE or IC design experience is required. You can get related training from daily work and also by internal trainings.

PV T3+

Job Description Summary

This position is a product validation (PV) engineer in R&D group who will be working on product validation for Conformal tool sets (LEC, ECO, CCD, CLP and Litmus). The validation includes Cadence full flow test and many advanced features under development.

The role will work very closely with tool programmers and product engineers to conduct effective and efficient test on Conformal tools to ensure tool quality and performance. Automation and scripting capability is a must like Linux Shell, Python or Perl.

Experienced digital IC designers or CAD, flow engineers with logic synthesis, equivalence check, low power design, UPF, timing constraints, STA experience will be preferable.

Familiar with Cadence tool like Conformal, Genus or Genus will be a big plus.

Job Description

This role will contribute to Conformal and Cadence full flow validation. The daily work will include testcase generation and testing by automation. The position will be responsible to ensure the tool quality by making test plans and executes with good efficiency and best automation to achieve best test coverage.

The candidate has to be familiar with modern digital chip design flow and has to be good at Linux Shell scripting or other scripting languages like Perl or Python. Other programming language like Web, JavaScript or HTML is a plus.

Formal verification experience using Conformal product families (LEC, ECO, CLP and CCD) is a plus.

Requirements:

- Fast learning and passion in EDA
- BS/MS/PhD in EE/ECE/CS or related field with sufficient programming skills especially Python/Perl and Linux Shell will be required to take this challenge.
- Candidates need to be familiar with chip design or CAD/EDA related usage or SoC design including STA, synthesis constraints (SDC) and logic optimization.

PE T2+

Job Description Summary

This position is a product engineer in R&D group who will be an expert in Conformal tool set including LEC, ECO and CLP. The role will be responsible for defining tool specification and collaborate with tool programmers to deliver complete solution to customers.

The role will be working with global top tier customers to deploy Conformal and Cadence full flow successfully at customers side and ensure they work smoothly.

Job Description:

Responsible to build up Conformal expertise among regional field teams and customers. Communicate effectively with customers and field teams on Conformal updates, projects, and promotions. Deliver Conformal trainings and seminars. Capture customer feedback and issues clearly with possibly succinct testcases to facilitate tool improvements and developments. Have strong background in ASIC implementation (synthesis, low-power, and ECO) and verification flow. Help to lead project/feature development from conception to deployment. Work closely with SJ and Taiwan R&D groups to prioritize customer issues and enhancements.

Requirements:

- Linux and Shell scripting is essential.
- Better to have good hands-on experience with EDA tools especially synthesis and formal verification like Conformal in previous work experiences.
- Must have good writing skills to create internal and external documentation.
- Better to have Tcl programming skills.
- Python programming skills a big plus!

We can also consider a full programming role if candidate is strong enough in Python/Tcl programming.

If you are a digital IC designer or CAD engineer and would like to elevate yourself to upper stream of the chip design industry, please join us to grow together and make bigger impact to the world! You will find bigger scope and develop (not only to learn) the best design technology to enable next generation chip designs!

Technical Writer**Job Description Summary**

This position is a technical writer in product engineering department of R&D group. The role will be working with R&D team to prepare documentation including technical specification, user manuals and training materials. The candidate should have strong English reading, writing and oral skills especially technical writing experience. Work experience in semiconductor or chip design industry is a plus.

Job Description

We are looking for a technical writer to help engineering team for drafting, polishing and publishing all documents. This role will be responsible for documentation like technical specification, user guides, application notes and design documents. The role will write and publish documents using commercial tools like FramMaker and also other languages like Markdown and HTML. The candidate must be an experienced technical writer in related industry and have passion to learn cutting edge technology through daily work. Familiar with Linux and HTML or Markdown language is a big plus, although not necessary. The candidate must submit at least one valid certification with scores from TOEFL, TOEIC or IELTS.

Requirements:

- 1+ year technical writing work experience
- Good reading, writing and oral skills in English (TOEFL > 95 TOEIC > 900 or IELTS > 8)
- Fast learning and passion in learning new technology

- Semiconductor related experience is a big plus

RD RTL T2+

Job Description Summary

This position is a software programmer who will be responsible for Conformal front end modules including parsing, elaborating HDL languages like SystemVerilog, Verilog, VHDL, SPICE and building internal database for all Conformal engines.

We are also looking for a Linux programmer who are experienced in working with open source codes like GNU utils or packages like DMTCP (<http://dmtcp.sourceforge.net/>, Distributed MultiThreaded CheckPointing) under Linux.

Job Description:

This role will be a member of RTL team in Conformal R&D group. The team develops front end packages of all Conformal tools (LEC/ECO/CLP/CCD/LITMUS). The role requires strong knowledge of Parser, Compiler and HDL language construct and also good knowledge of data structure. Candidates who have compiler, open source porting, Lex/Yacc experiences will be a big plus.

Requirements:

- Good C/C++ Programming capability
- Familiar with Verilog/SystemVerilog or Linux programming
- Experience in Parallel/distributed programming is a big plus
- Compiler experience is a bug plus

RD T2+ (For all CFM teams except PV,PE)

Job Description Summary

This position is a software programmer who will develop best-in-class EDA tools that enable modern chip design. We tackle a breadth of challenging projects all in an enjoyable and family-friendly environment. We develop must-have IC design tools (LEC/CECO/CLP/CCD/LITMUS), next-generation verification technologies, and we influence the direction of verification methodologies. If you are a strong programmer in C/C++, welcome to join us to make history, together with the best team in the world.

Job Description

Conformal team is looking for best talents in Taiwan. We need you to become a part of Conformal team and help delivering the best chip design technology in the world and grow your career with this high performance team which is one of the best in the area!

The Conformal team is a diverse group of innovative, talented and passionate engineers committed to delivering cutting-edge verification technologies and trusted solutions to key high-technology customers around the world. We tackle a breadth of challenging projects all in an enjoyable and family-friendly environment. We develop must-have IC design tools

(LEC/CECO/CLP/CCD/LITMUS), next-generation verification technologies, and we influence the direction of verification methodologies.

As a part of this team, you will develop, implement, and test advanced algorithms in timing constraint verification, logic verification, equivalence check, data-path verification, logic synthesis, SAT solver, boolean optimization and related areas.

Requirements:

- Fast learning and passion in problem solving, good at algorithm
- Good C/C++ programming skills is essential.
- EDA background in school or CS with strong C/C++ or Python programming capability
- If you are without above prerequisite and you are confident being a good programmer of C++ and a quick learner, please still submit your resume to us!